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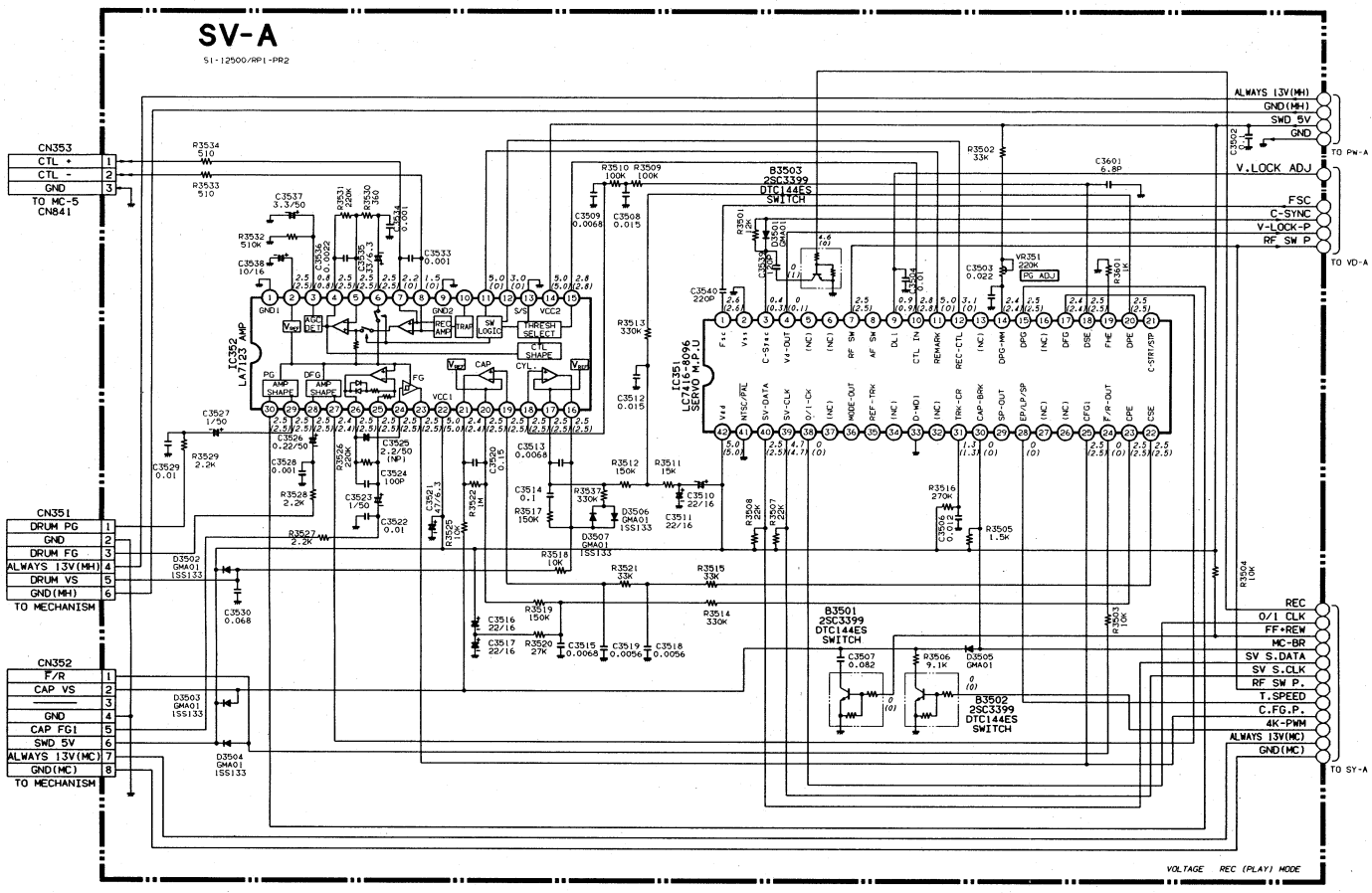
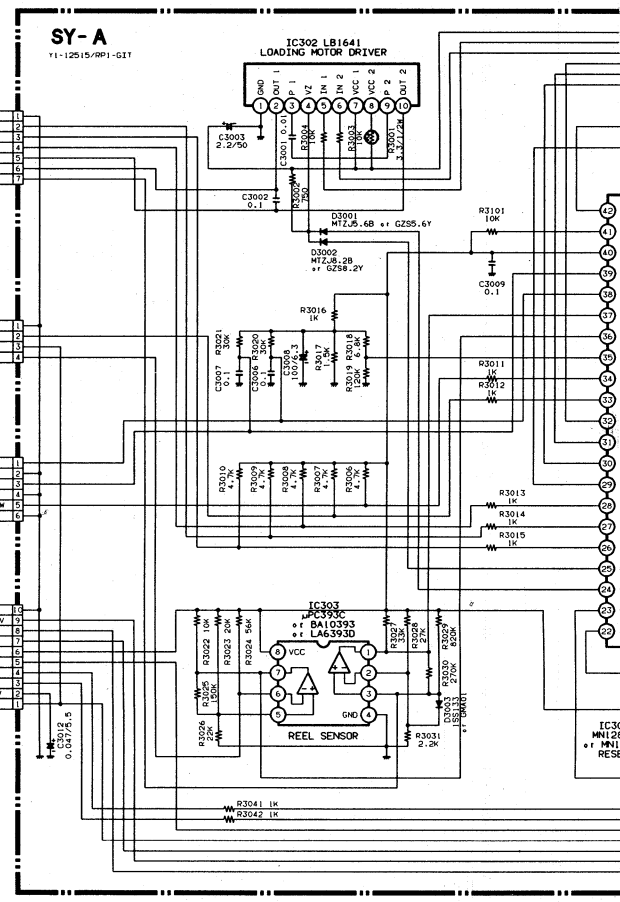
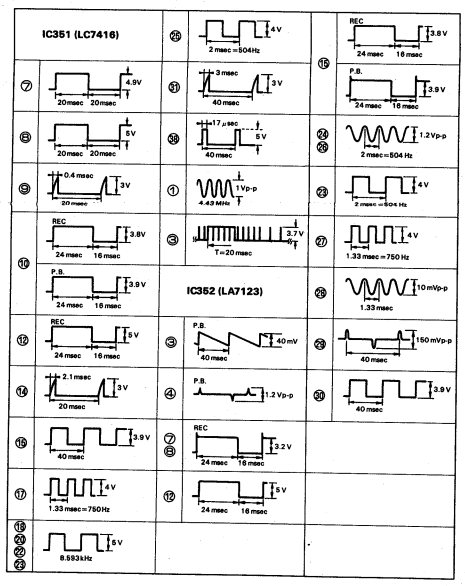
CP-1 BOARD (SV-A) SERVO CIRCUIT DIAGRAM



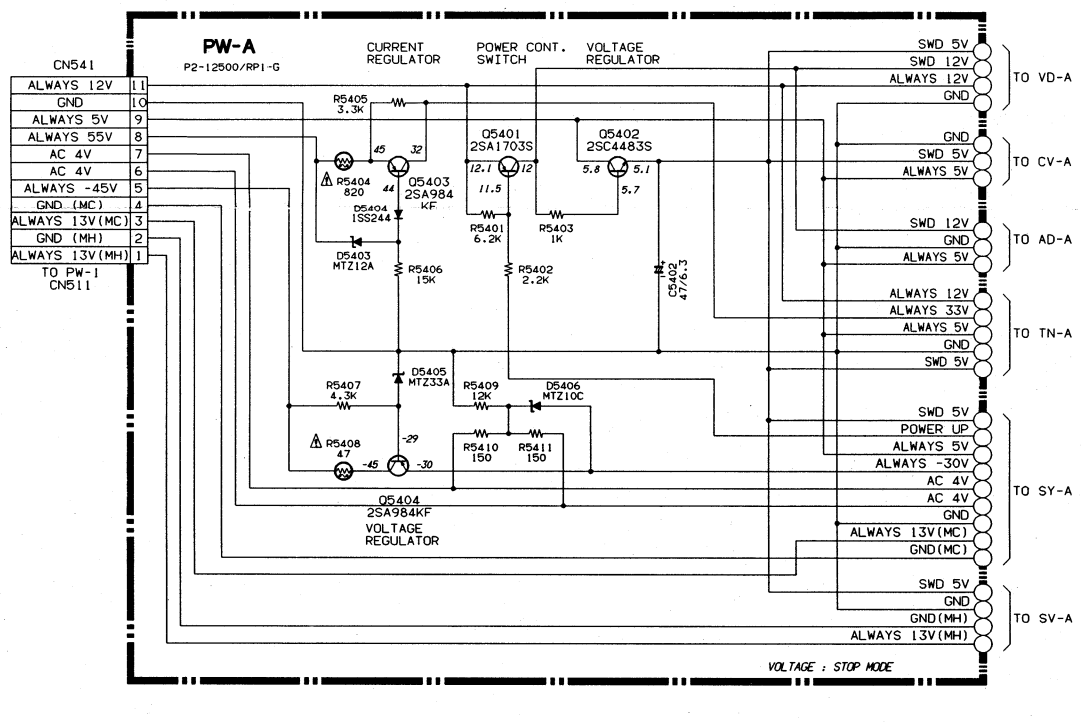
CP-1 BOARD (SY-A) SYSTE



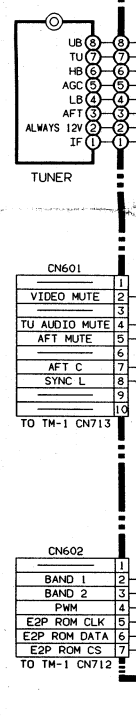
WAVEFORM OF SERVO



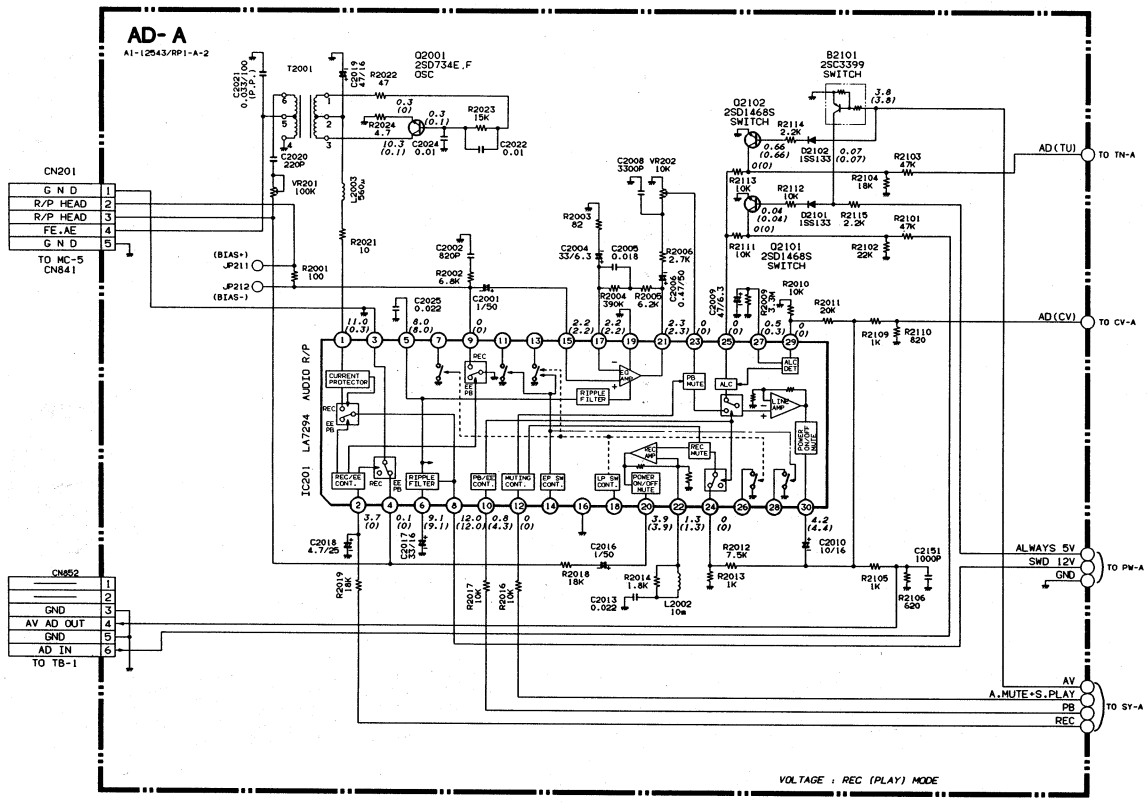
CP-1 BOARD (PW-A) POWER SUPPLY CIRCUIT DIAGRAM



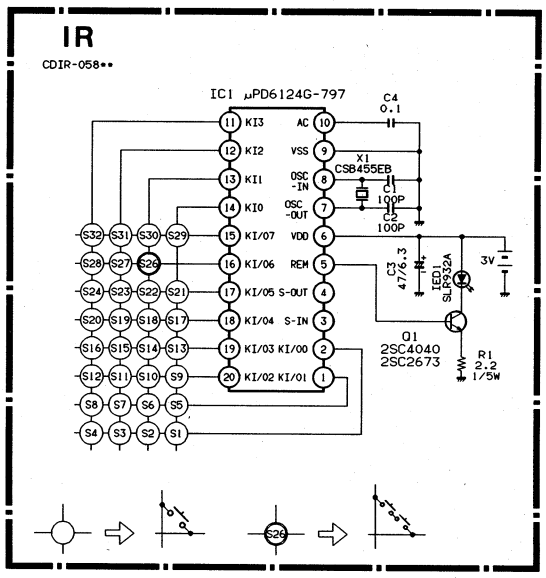
CP-1 BO



CP-1 BOARD (AD-A) AUDIO CIRCUIT DIAGRAM



IR REMOTE CONTROL CIRCUIT DIAGRAM

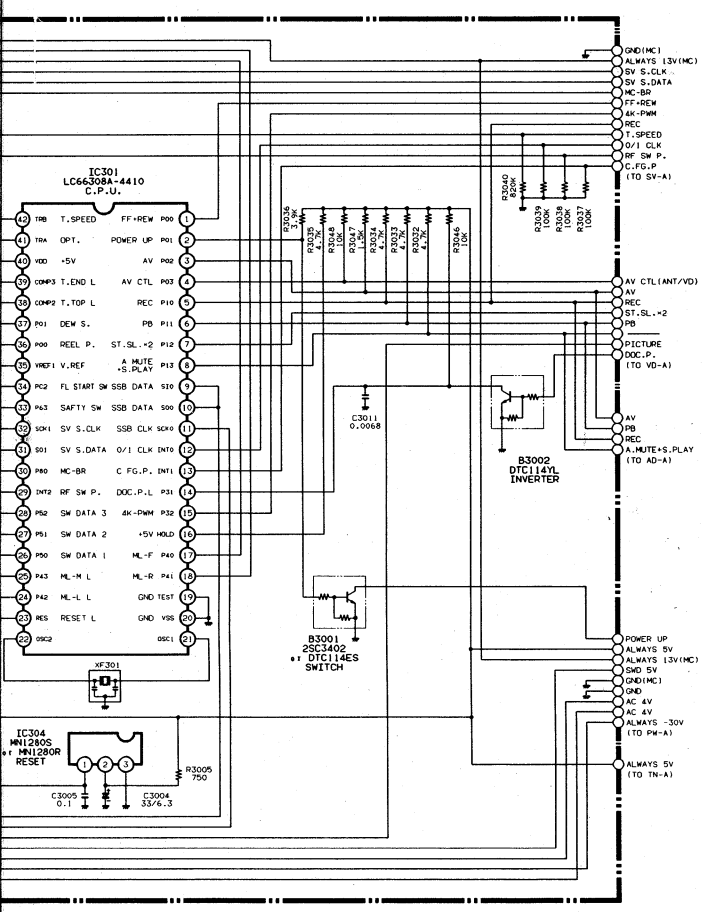


AVAILABLE KEY FUNCTION OF REMOTE CONTROL UNIT

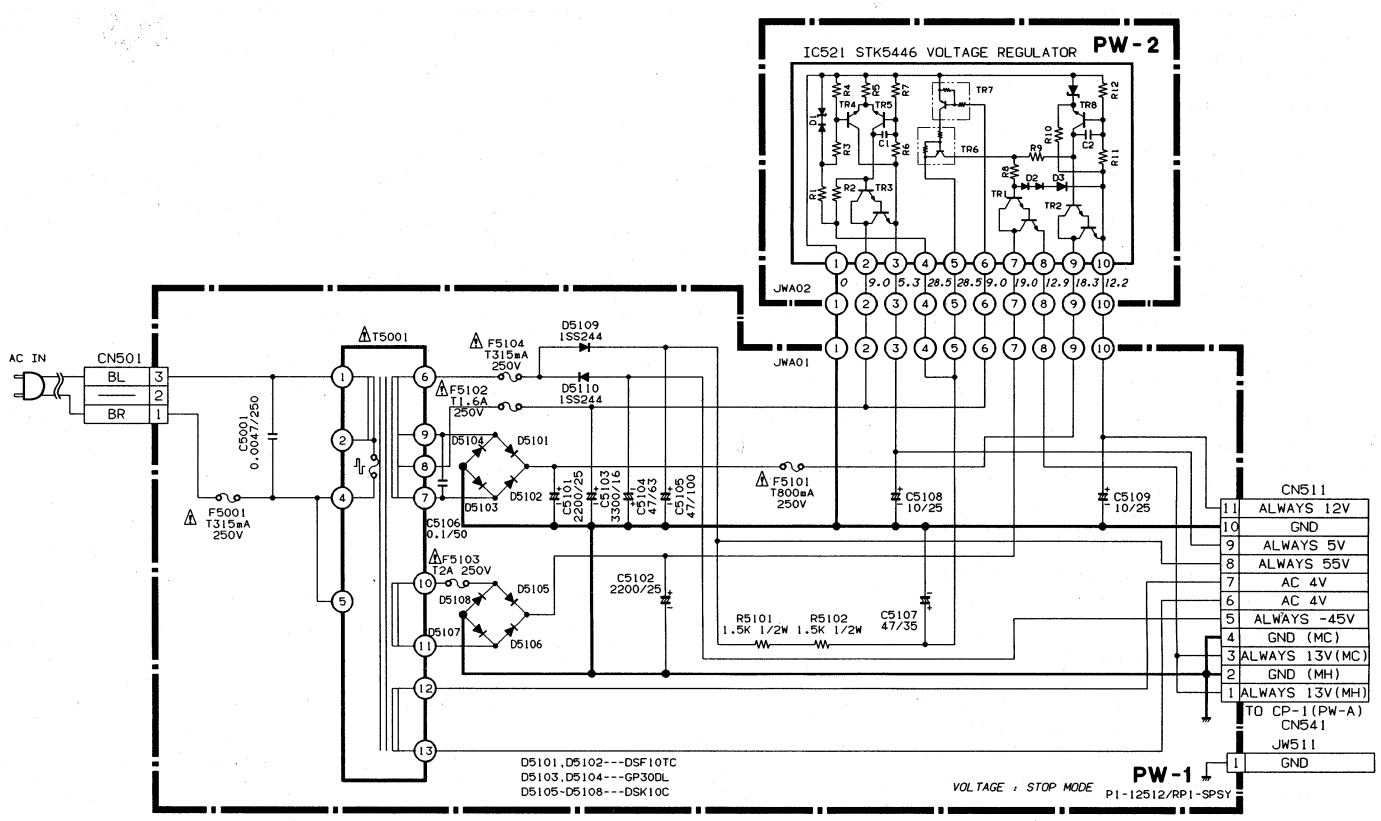
KEY NO	FUNCTION	
S1	POWER	USE
S2	VTR/TV	
S3	RESET	USE
S4	MEMORY	USE
S5	PROG	USE
S6	1	USE
S7	2	USE
S8	3	USE
S9	CLOCK	USE
S10	4	USE
S11	5	USE
S12	6	USE
S13	VPS	
S14	7	USE
S15	8	USE
S16	9	USE
S17	CANCEL	USE
S18	⇤	USE
S19	0/AV	USE
S20	⇥	USE
S21	BLANK	USE
S22	REW	USE
S23	PLAY	USE
S24	FF	USE
S26	REC	USE
S27	STOP	USE
S28	PAUSE/STILL	USE
S29	SP/LP	
S30	REMAIN	
S31	TRACK	
S32	MONITOR	USE

IR-05800/RU2

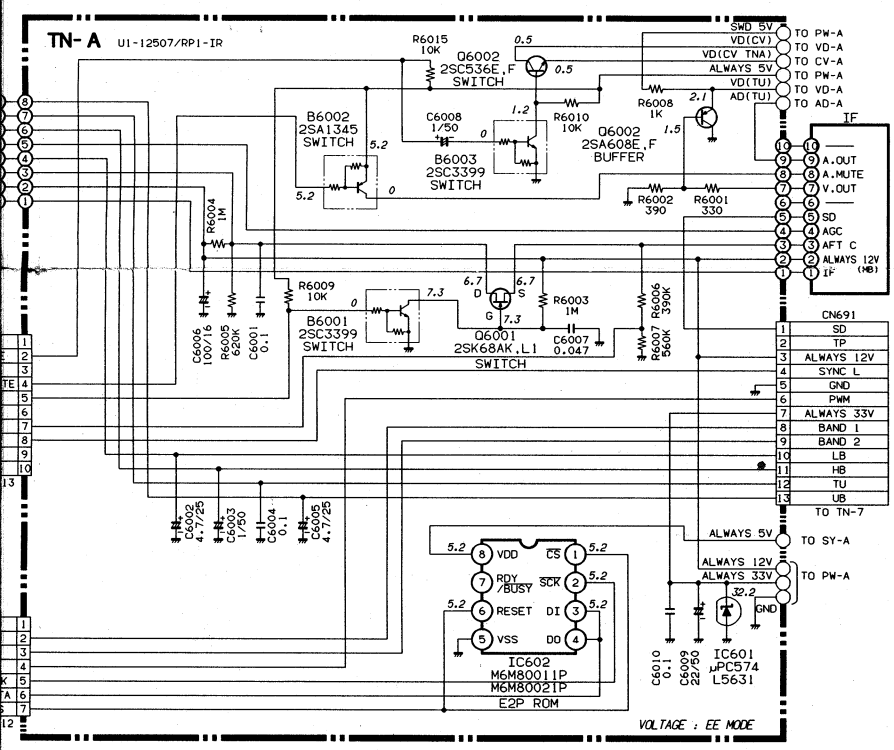
TEM CONTROL CIRCUIT DIAGRAM



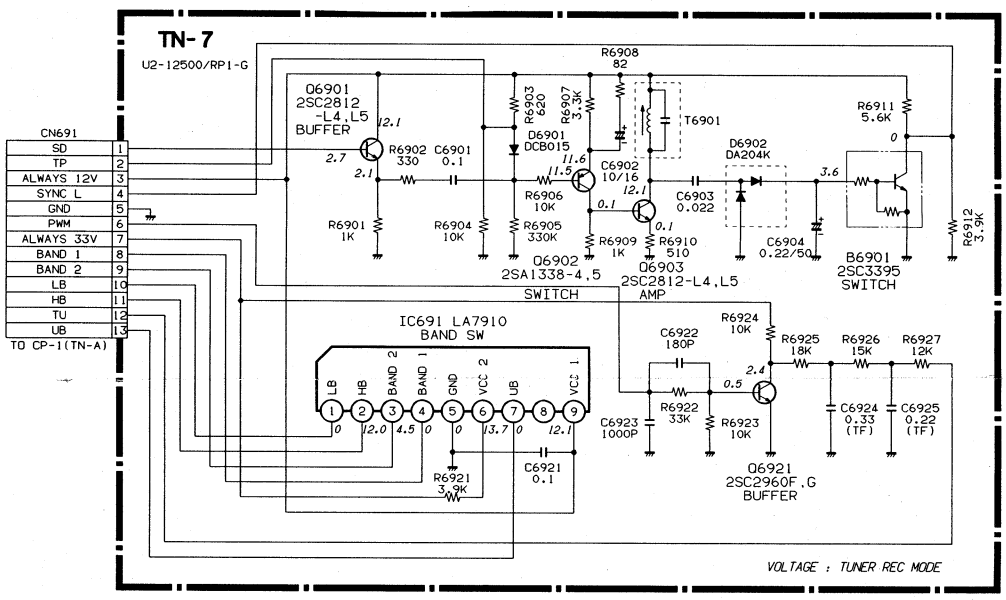
PW-1, 2 BOARD POWER SUPPLY CIRCUIT DIAGRAM



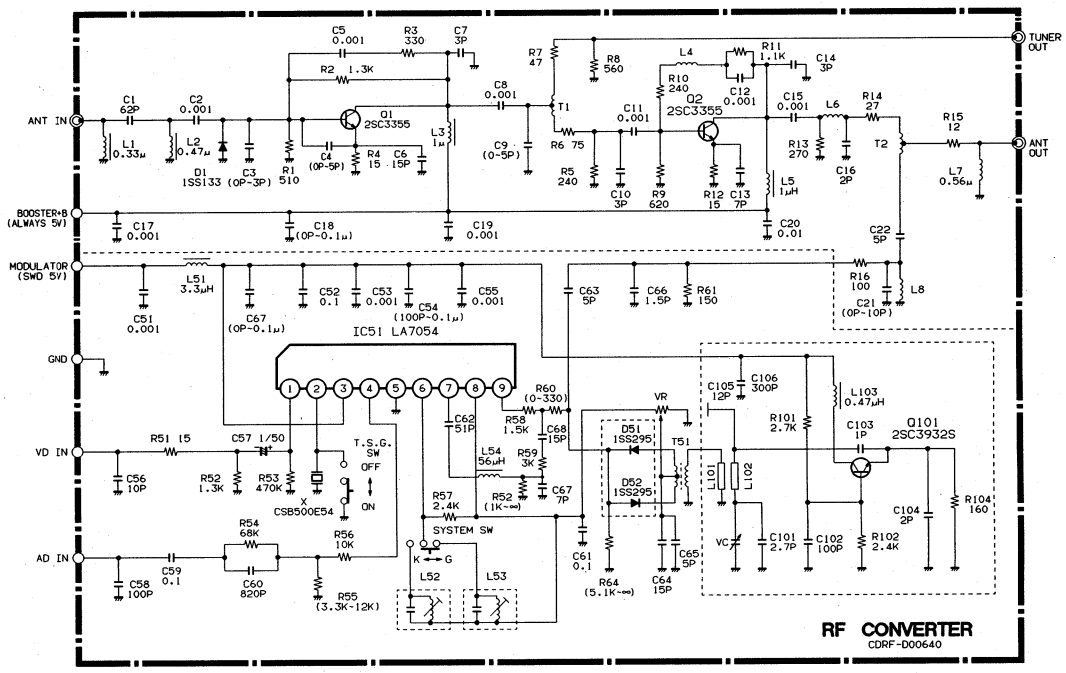
BOARD (TN-A) TUNER IF CONNECTION CIRCUIT DIAGRAM



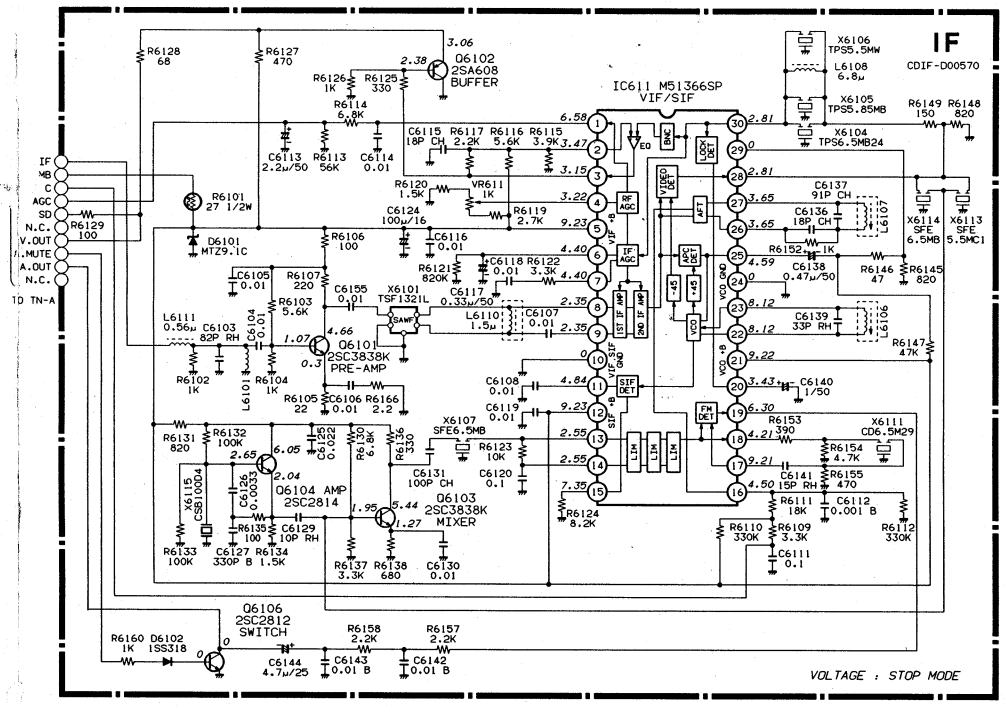
TN-7 BOARD TUNING DET. & SYNC DET. CIRCUIT DIAGRAM



RF CONVERTER CIRCUIT



IF CIRCUIT DIAGRAM



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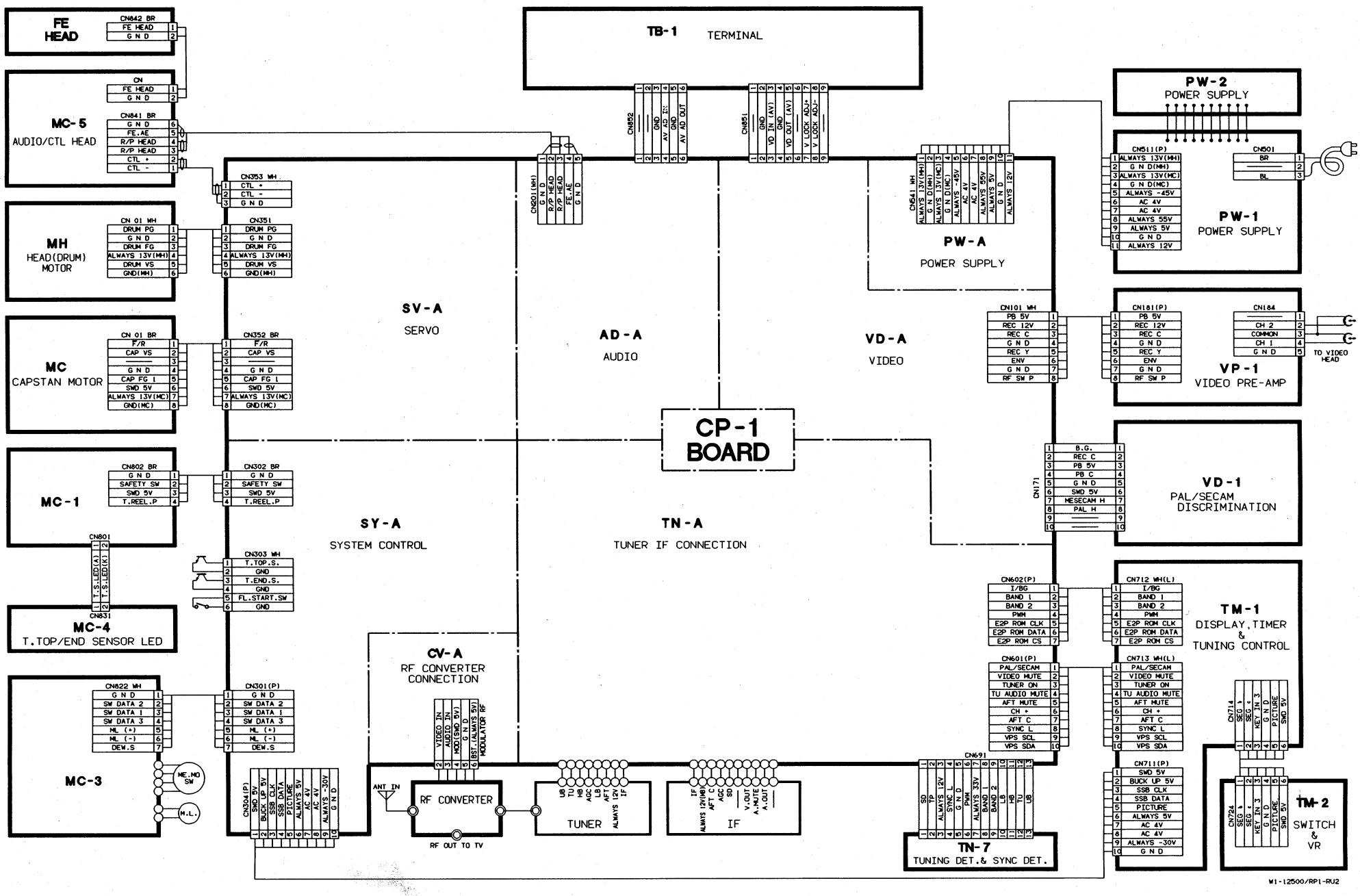
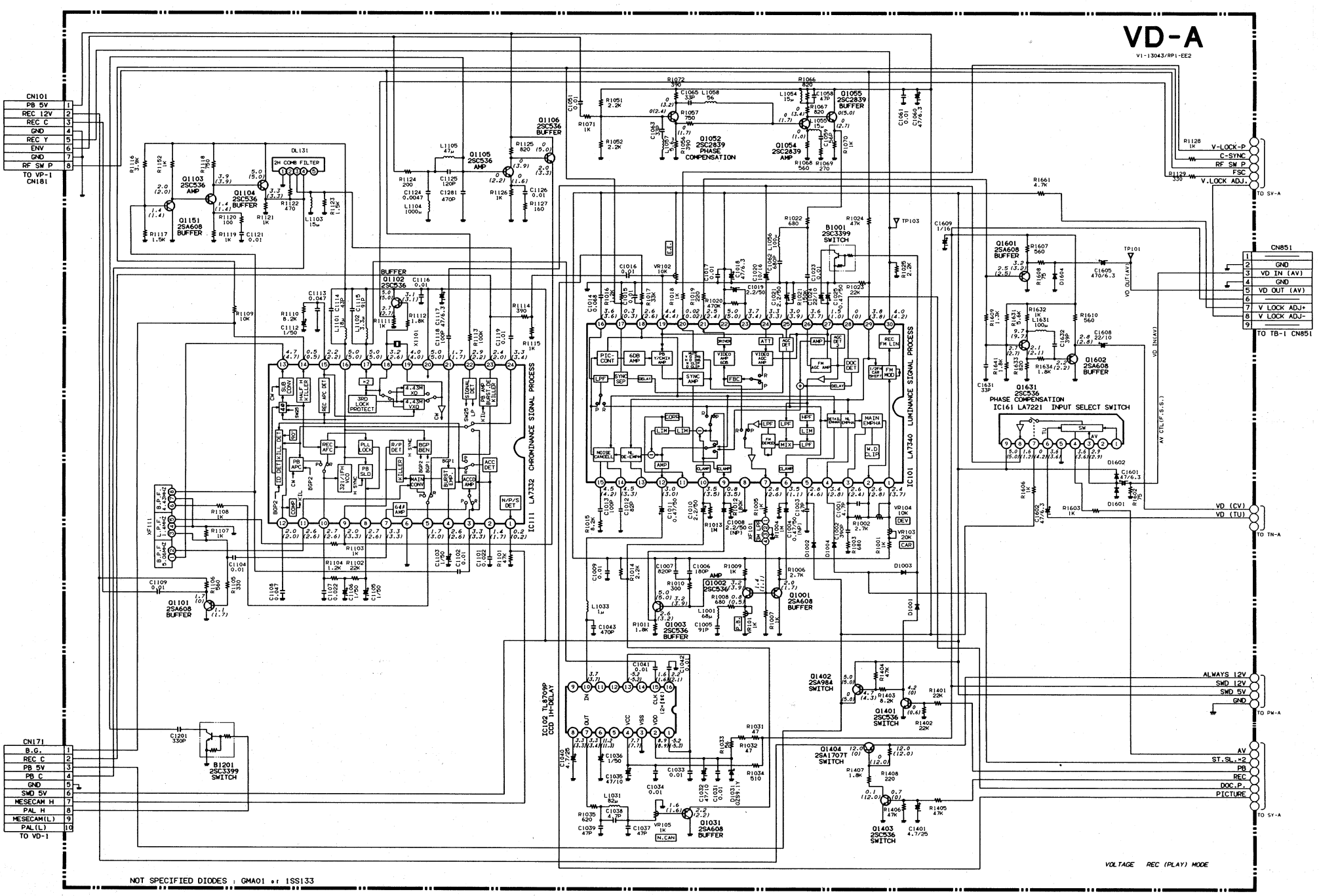
OVERALL WIRING DIAGRAM

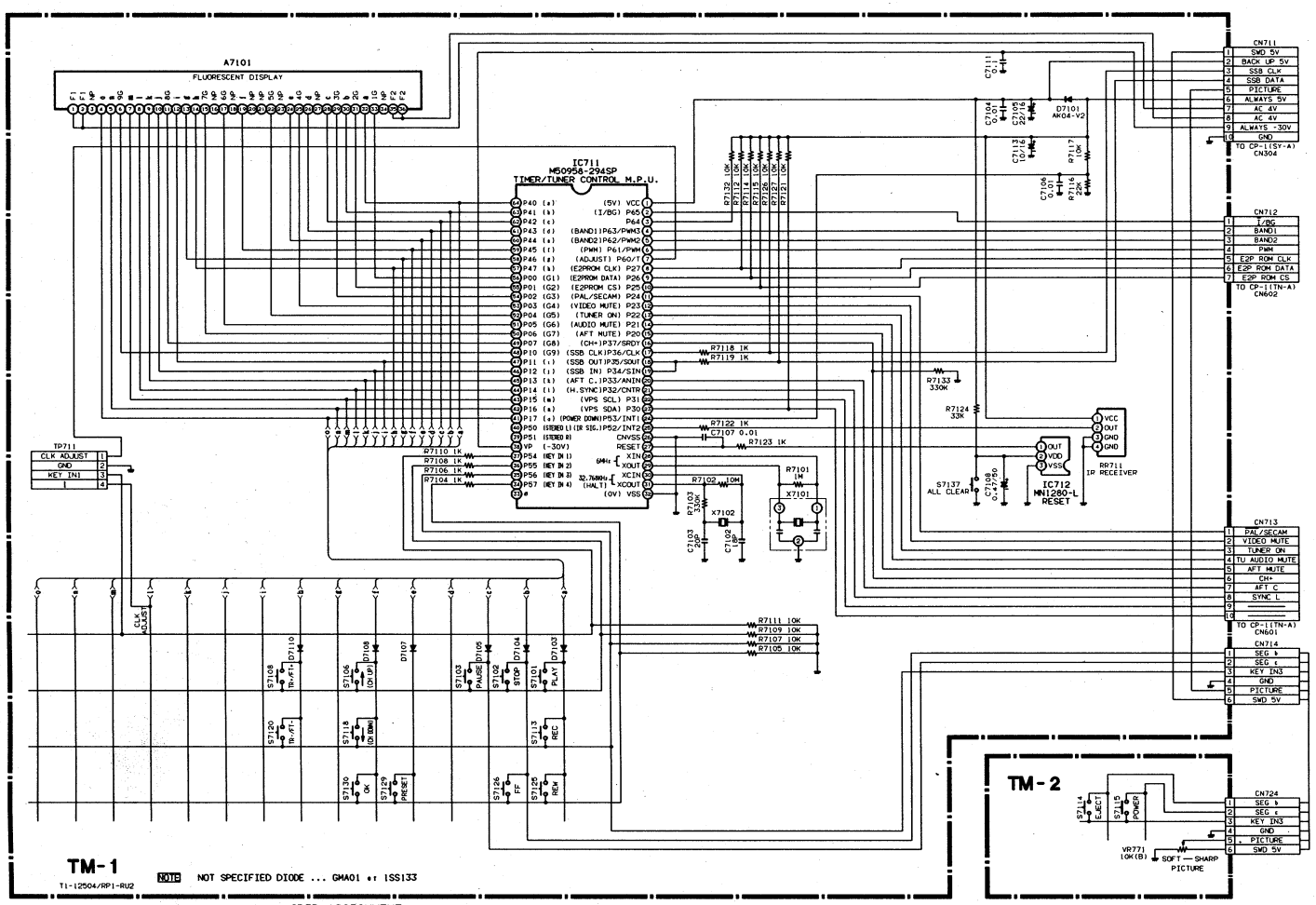


W1-12500/RP1-RU2

CP-1 BOARD (VD-A) VIDEO CIRCUIT DIAGRAM



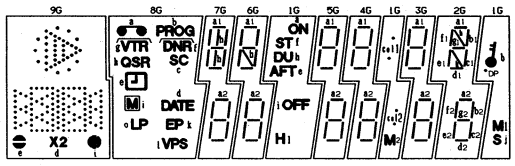
TM-1 BOARD DISPLAY, TIMER & TUNING CONTROL CIRCUIT DIAGRAM
TM-2 BOARD SWITCH & VR CIRUIT DIAGRAM



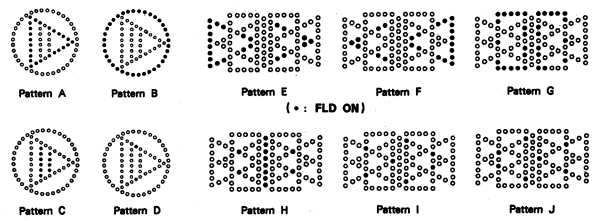
ANODE CONNECTION

	9G	8G	7G	6G	5G	4G	3G	2G	1G
a	B		a1	a1	a1	a1	a1	a1	ON
b	A	PROG.	b1	b1	b1	b1	b1	b1	
c	-	SC	c1	c1	c1	c1	c1	c1	DP
d	×2	DATE	d1	d1	d1	d1	d1	d1	M(1)
e			e1	e1	e1	e1	e1	e1	AFT
f	C	DNR	f1	f1	f1	f1	f1	f1	ST
g	D	VTR	g1	g1	g1	g1	g1	g1	colon1
h	-	QSR	/	\	-	-	-	\	DU
i			a2	a2	a2	a2	a2	a2	OFF
j	E	-	b2	b2	b2	b2	b2	b2	S
k	I	EP	c2	c2	c2	c2	c2	c2	-
l	H	VPS	d2	d2	d2	d2	d2	d2	H
m	J	-	e2	e2	e2	e2	e2	e2	M(2)
n	F	-	f2	f2	f2	f2	f2	f2	-
o	G	LP	g2	g2	g2	g2	g2	g2	colon2

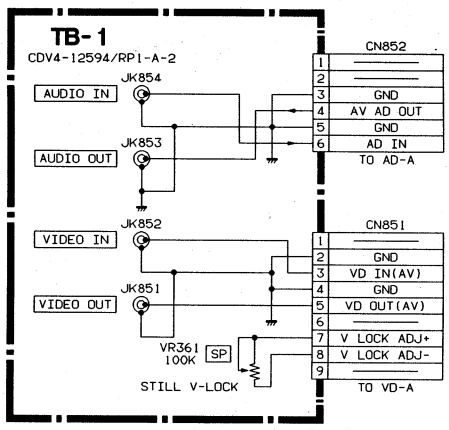
GRID ASSIGNMENT



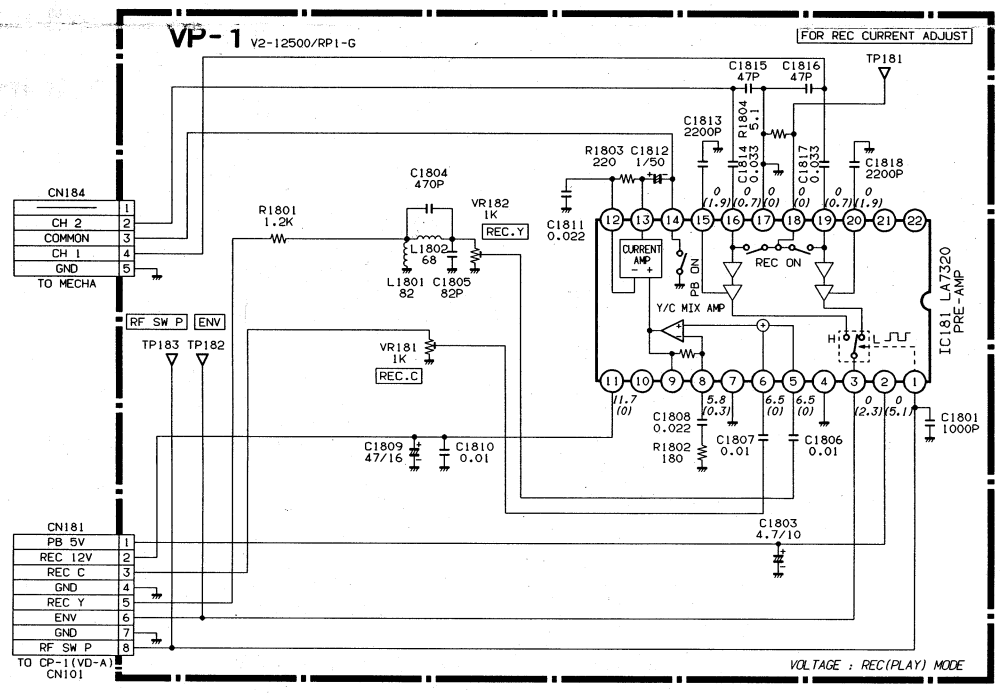
9G: GRAPHIC MODE DISPLAY



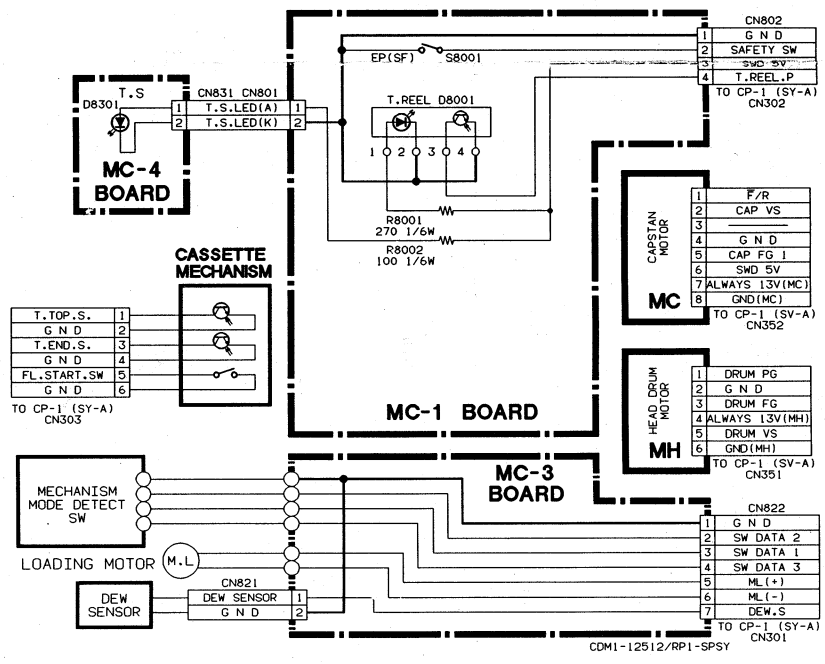
TB-1 BOARD TERMINAL CIRUIT DIAGRAM



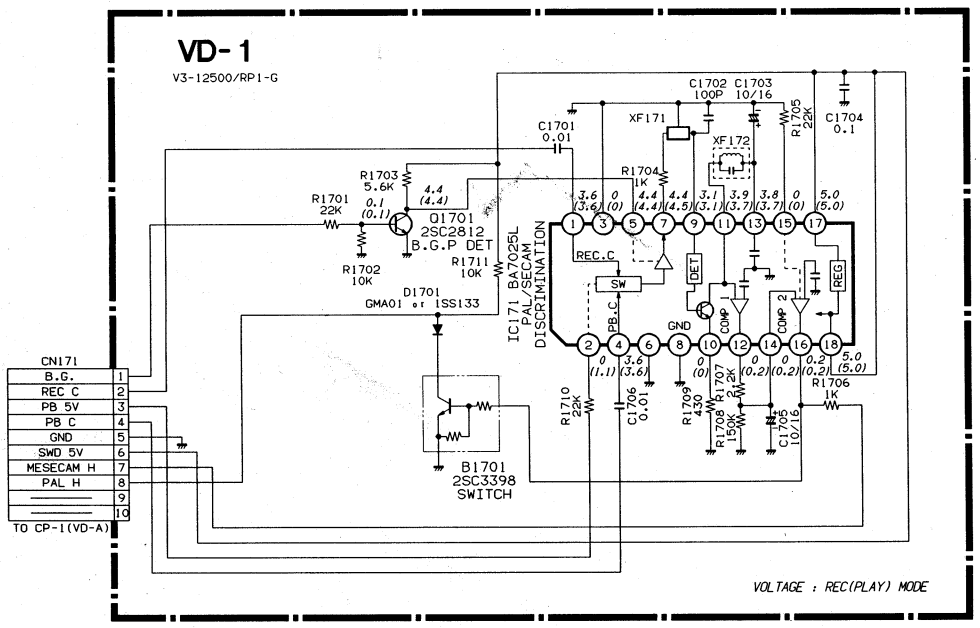
VP-1 BOARD VIDEO PRE-AMP CIRUIT DIAGRAM



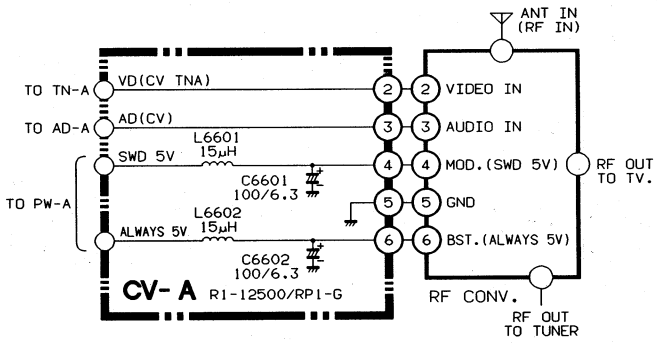
MECHANISM CONNECTION CIRCUIT



VD-1 BOARD PAL/SECAM DISCRIMINATION CIRUIT DIAGRAM



CP-1 BOARD (CV-A) RF CONVERTER CONNECTION CIRCUIT DIAGRAM



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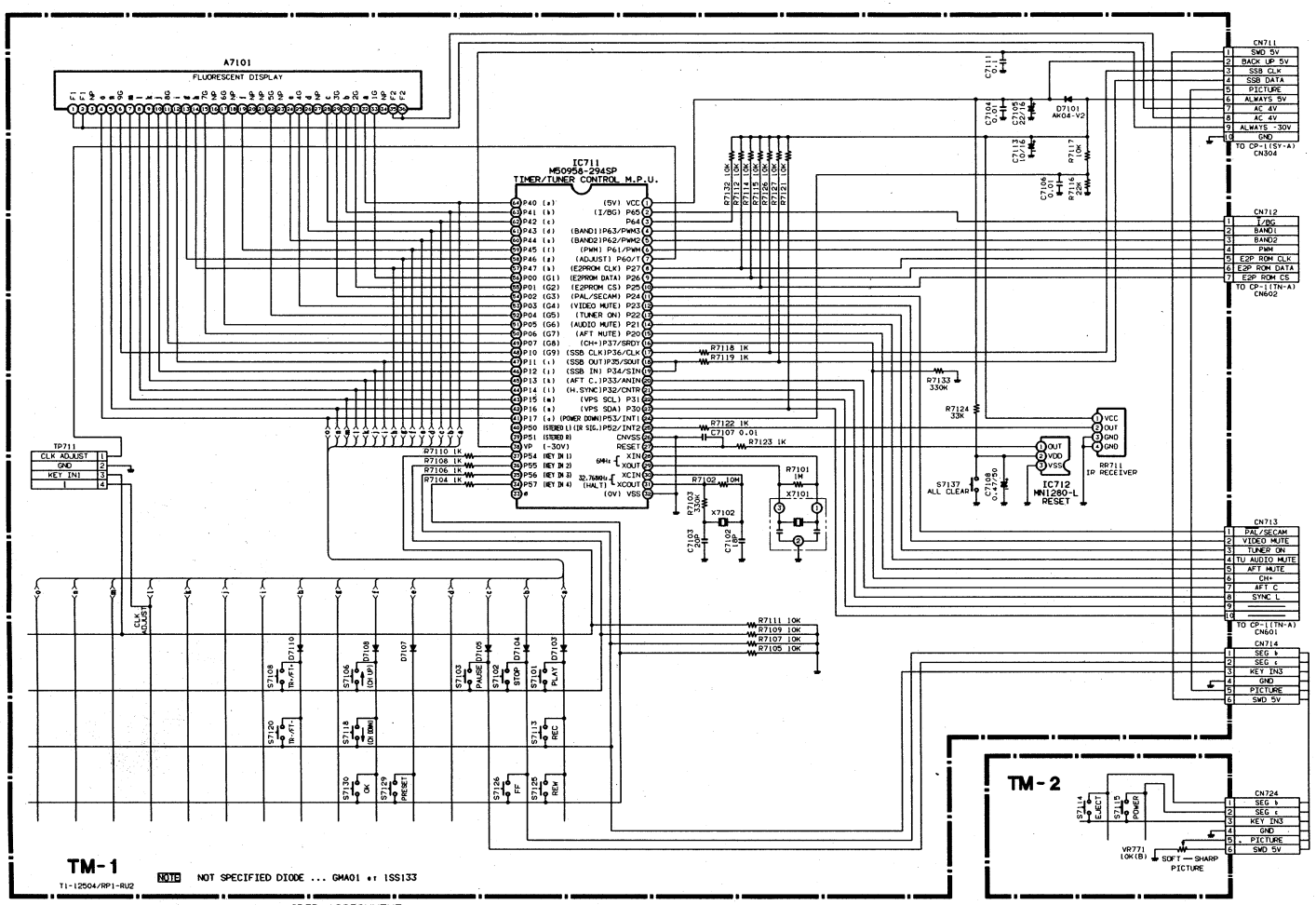
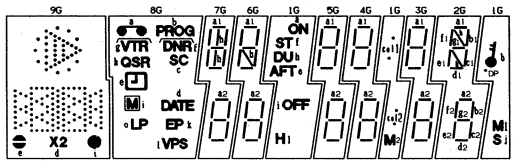
TM-1 BOARD DISPLAY, TIMER & TUNING CONTROL CIRCUIT DIAGRAM
TM-2 BOARD SWITCH & VR CIRUIT DIAGRAM



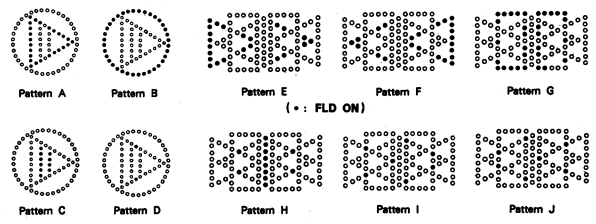
ANODE CONNECTION

	9G	8G	7G	6G	5G	4G	3G	2G	1G
a	B		a1	a1	a1	a1	a1	a1	ON
b	A	PROG.	b1	b1	b1	b1	b1	b1	
c	-	SC	c1	c1	c1	c1	c1	c1	DP
d	×2	DATE	d1	d1	d1	d1	d1	d1	M(1)
e			e1	e1	e1	e1	e1	e1	AFT
f	C	DNR	f1	f1	f1	f1	f1	f1	ST
g	D	VTR	g1	g1	g1	g1	g1	g1	colon1
h	-	QSR	/	\	-	-	-	\	DU
i		M	a2	a2	a2	a2	a2	a2	OFF
j	E	-	b2	b2	b2	b2	b2	b2	S
k	I	EP	c2	c2	c2	c2	c2	c2	-
l	H	VPS	d2	d2	d2	d2	d2	d2	H
m	J	-	e2	e2	e2	e2	e2	e2	M(2)
n	F	-	f2	f2	f2	f2	f2	f2	-
o	G	LP	g2	g2	g2	g2	g2	g2	colon2

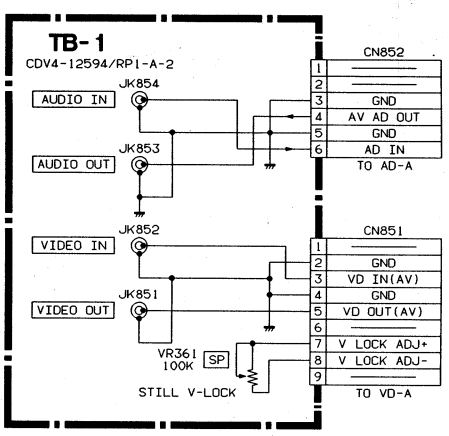
GRID ASSIGNMENT



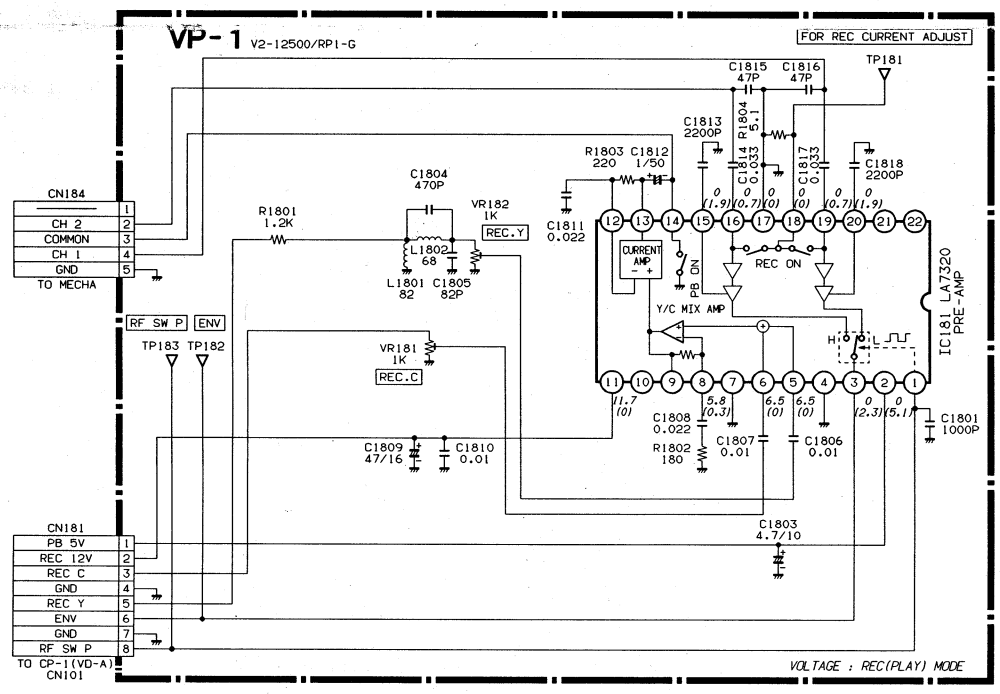
9G: GRAPHIC MODE DISPLAY



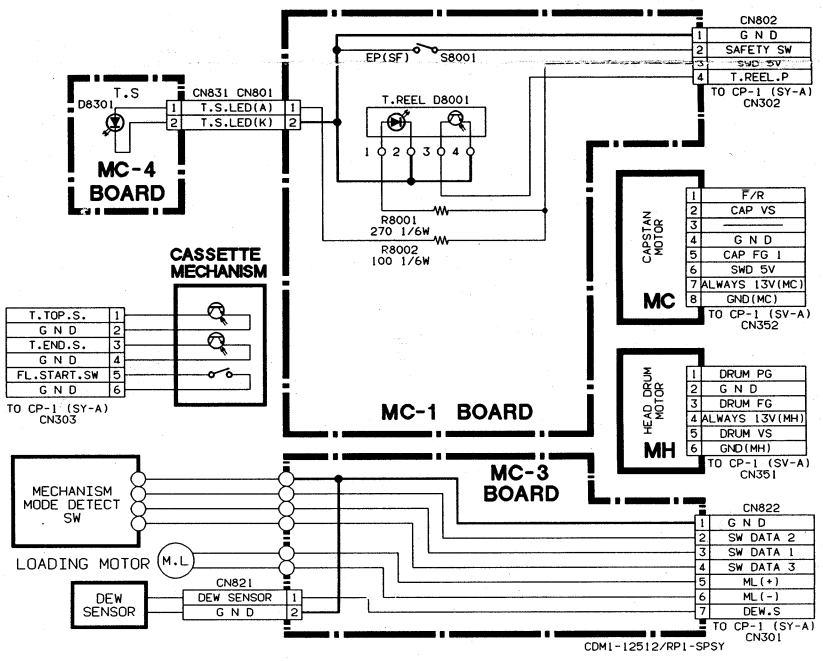
TB-1 BOARD TERMINAL CIRUIT DIAGRAM



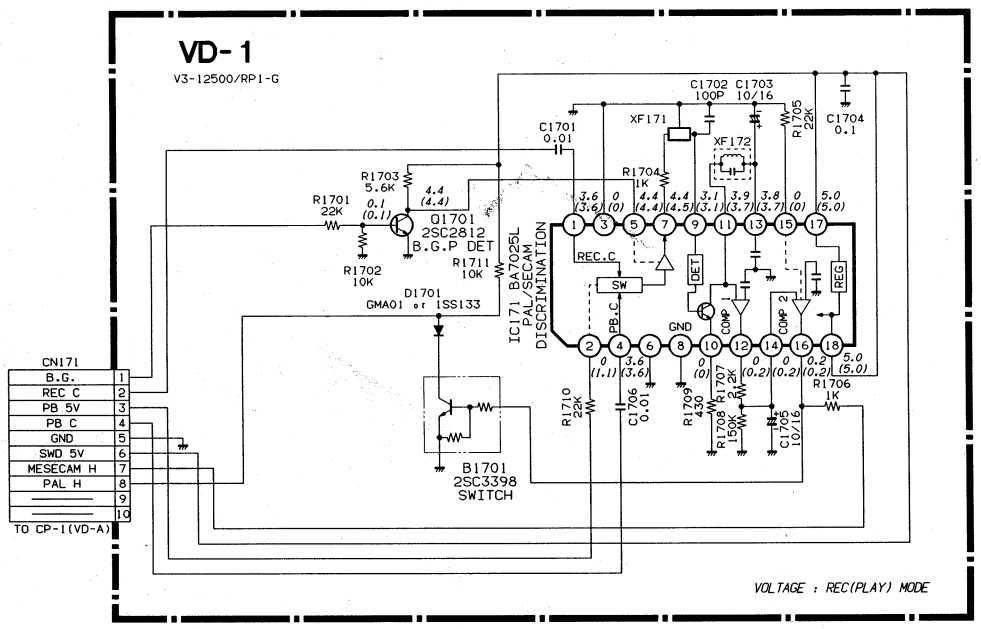
VP-1 BOARD VIDEO PRE-AMP CIRUIT DIAGRAM



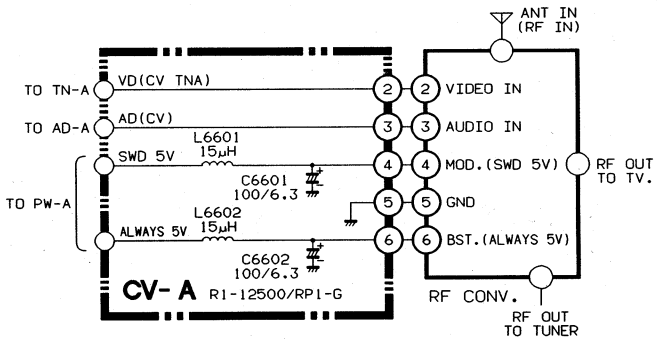
MECHANISM CONNECTION CIRCUIT



VD-1 BOARD PAL/SECAM DISCRIMINATION CIRUIT DIAGRAM



CP-1 BOARD (CV-A) RF CONVERTER CONNECTION CIRCUIT DIAGRAM



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